



## Why DELTA gives you the best solution

### Foundry

- TSMC
- IBM
- Global Foundries

### Packaging

- In-house experts
- Feasibility study
- Direct access to ASE
- Prototyping in-house

### Testing

- In-house 10 testers (wafer, component)
- 25 years of test development
- State of the art – Verigy 93K
- 12" wafer prober
- Clean room

### Failure analysis

- All in-house
- Skilled engineers
- Fast and effective service

### ASIC design

- All in-house
- 0.6 – 0.35 – 0.18 – 0.13  $\mu\text{m}$  designs
- Mixed signal, low power, wireless, sensors, RFID, optical
- Skilled engineers
- Cadence design platform

### Other

- Professional project leaders
- Logistics team – in-house
- European level service and quality
- Wafer, component storage facility
- 25 years of experience

### Business models

- ASIC
- COT
- ASIC -> COT
- Route to the Far East
- VMI

### Communication

- Same time-zone
- Language: English, German, French, Spanish, Hebrew, Thai, Mandarin, Chinese and Danish
- Short travelling time



GLOBAL  
FOUNDRIES

### DELTA

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